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November 2013

FQP33N10

N-Channel QFET[®] MOSFET 100 V, 33 A, 52 m Ω

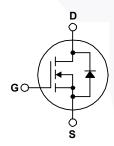
Description

This N-Channel enhancement mode power MOSFET is produced using Fairchild Semiconductor's proprietary planar stripe and DMOS technology. This advanced MOSFET technology has been especially tailored to reduce on-state resistance, and to provide superior switching performance and high avalanche energy strength. These devices are suitable for switched mode power supplies, audio amplifier, DC motor control, and variable switching power applications.

Features

- 33 A, 100 V, $R_{DS(on)}$ = 52 m Ω (Max.) @ V_{GS} = 10 V, I_D = 16.5 A
- · Low Gate Charge (Typ. 38 nC)
- · Low Crss (Typ. 62 pF)
- · 100% Avalanche Tested
- · 175°C Maximum Junction Temperature Rating





Absolute Maximum Ratings T_C = 25°C unless otherwise noted.

Symbol	Parameter		FQP33N10	Unit
V _{DSS}	Drain-Source Voltage		100	V
I _D	Drain Current - Continuous (T _C = 25°C)	33	Α
	- Continuous (T _C = 100°	C)	23	Α
I _{DM}	Drain Current - Pulsed	(Note 1)	132	Α
V _{GSS}	Gate-Source Voltage		± 25	V
E _{AS}	Single Pulsed Avalanche Energy	(Note 2)	435	mJ
I _{AR}	Avalanche Current	(Note 1)	33	Α
E _{AR}	Repetitive Avalanche Energy	(Note 1)	12.7	mJ
dv/dt	Peak Diode Recovery dv/dt	(Note 3)	6.0	V/ns
P_{D}	Power Dissipation (T _C = 25°C)		127	W
	- Derate above 25°C		0.85	W/°C
T _J , T _{STG}	Operating and Storage Temperature Rang	е	-55 to +175	°C
T _L	Maximum Lead Temperature for Soldering, 1/8" from Case for 5 seconds		300	°C

Thermal Characteristics

Symbol	Parameter	FQP33N10	Unit	
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case, Max.	1.18	°C/W	
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient, Max.	62.5	°C/W	

Package Marking and Ordering Information

Part Number	Top Mark	Package	Packing Method	Reel Size	Tape Width	Quantity
FQP33N10	FQP33N10	TO-220	Tube	N/A	N/A	50 units

Electrical Characteristics

 $T_C = 25$ °C unless otherwise noted.

Symbol	Parameter Test Conditions		Min	Тур	Max	Uni
Off Cha	aracteristics					
BV _{DSS}	Drain-Source Breakdown Voltage	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	100			V
ΔBV _{DSS} / ΔT _J	Breakdown Voltage Temperature Coefficient	I _D = 250 μA, Referenced to 25°C		0.11		V/°C
I _{DSS}	Zero Coto Valtorio Duoin Cumunt	V _{DS} = 100 V, V _{GS} = 0 V			1	μΑ
	Zero Gate Voltage Drain Current	V _{DS} = 80 V, T _C = 150°C			10	μΑ
I _{GSSF}	Gate-Body Leakage Current, Forward	V _{GS} = 25 V, V _{DS} = 0 V			100	nA
I _{GSSR}	Gate-Body Leakage Current, Reverse	V _{GS} = -25 V, V _{DS} = 0 V			-100	nA
On Cha	racteristics					
V _{GS(th)}	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	2.0		4.0	V
R _{DS(on)}	Static Drain-Source On-Resistance	V _{GS} = 10 V, I _D = 16.5 A		0.040	0.052	Ω
g _{FS}	Forward Transconductance	V _{DS} = 40 V, I _D = 16.5 A	\	22		S
Dynam	ic Characteristics					
C _{iss}	Input Capacitance	$V_{DS} = 25 \text{ V}, V_{GS} = 0 \text{ V},$		1150	1500	pF
C _{oss}	Output Capacitance	f = 1.0 MHz		320	420	pF
C _{rss}	Reverse Transfer Capacitance			62	80	pF
Switch	ing Characteristics					
$t_{d(on)}$	Turn-On Delay Time	V _{DD} = 50 V, I _D = 33 A,		15	40	ns
t _r	Turn-On Rise Time	$R_{G} = 25 \Omega$		195	400	ns
$t_{d(off)}$	Turn-Off Delay Time			80	170	ns
t _f	Turn-Off Fall Time	(Note 4)	/	110	230	ns
Qg	Total Gate Charge	$V_{DS} = 80 \text{ V}, I_{D} = 33 \text{ A},$		38	51	nC
Q _{gs}	Gate-Source Charge	V _{GS} = 10 V	/	7.5		nC
Q _{ad}	Gate-Drain Charge	(Note 4)		18		nC

Drain-Source Diode Characteristics and Maximum Ratings

I _S	Maximum Continuous Drain-Source Diode Forward Current				33	Α
I _{SM}	Maximum Pulsed Drain-Source Diode F	orward Current			132	Α
V _{SD}	Drain-Source Diode Forward Voltage	V _{GS} = 0 V, I _S = 33 A			1.5	V
t _{rr}	Reverse Recovery Time	V _{GS} = 0 V, I _S = 33 A,		80		ns
Q _{rr}	Reverse Recovery Charge	$dI_F / dt = 100 A/\mu s$		0.22	/	μC

- Notes: Notes: Notes: Notes: A Repetitive Rating: Pulse width limited by maximum junction temperature.
 2. L = 0.6 mH, I_{AS} = 33 A, V_{DD} = 25 V, R_G = 25 Ω , starting T_J = 25°C.
 3. I_{SD} \leq 33 A, di/dt \leq 300 A/µs, V_{DD} \leq BV_{DSS}, starting T_J = 25°C.
 4. Essentially Independent of Operating Temperature.

Typical Characteristics

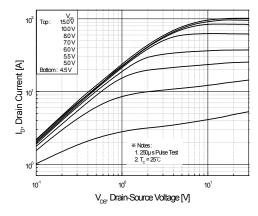


Figure 1. On-Region Characteristics

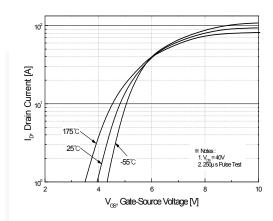


Figure 2. Transfer Characteristics

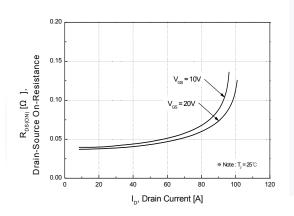


Figure 3. On-Resistance Variation vs.

Drain Current and Gate Voltage

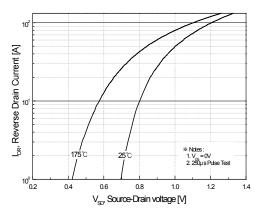


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature

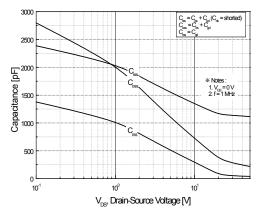


Figure 5. Capacitance Characteristics

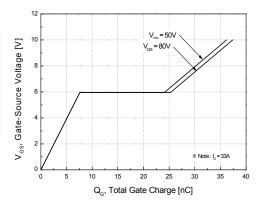
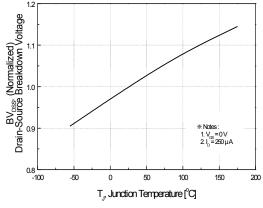


Figure 6. Gate Charge Characteristics

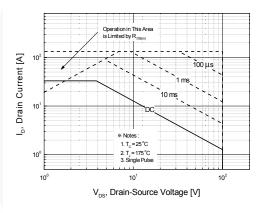
Typical Characteristics (continued)



30 25 (Dezignation Temperature [°C]

Figure 7. Breakdown Voltage Variation vs. Temperature

Figure 8. On-Resistance Variation vs. Temperature



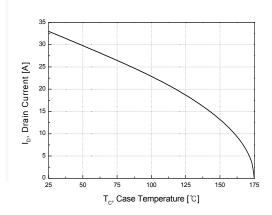


Figure 9. Maximum Safe Operating Area

Figure 10. Maximum Drain Current vs. Case Temperature

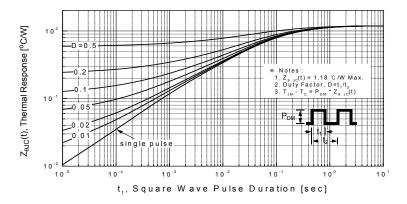


Figure 11. Transient Thermal Response Curve

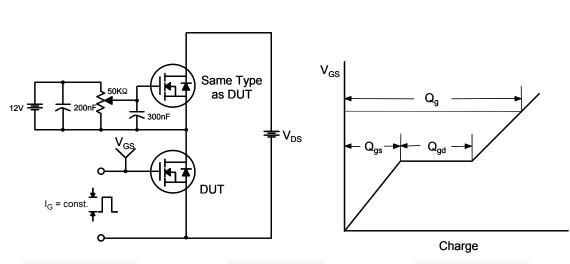


Figure 12. Gate Charge Test Circuit & Waveform

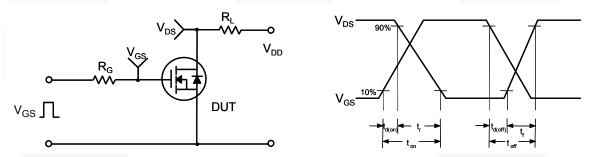


Figure 13. Resistive Switching Test Circuit & Waveforms

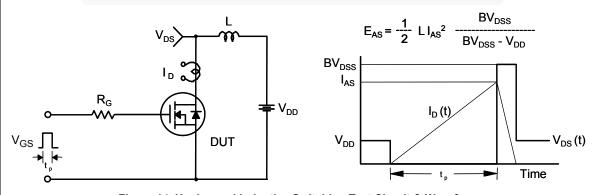
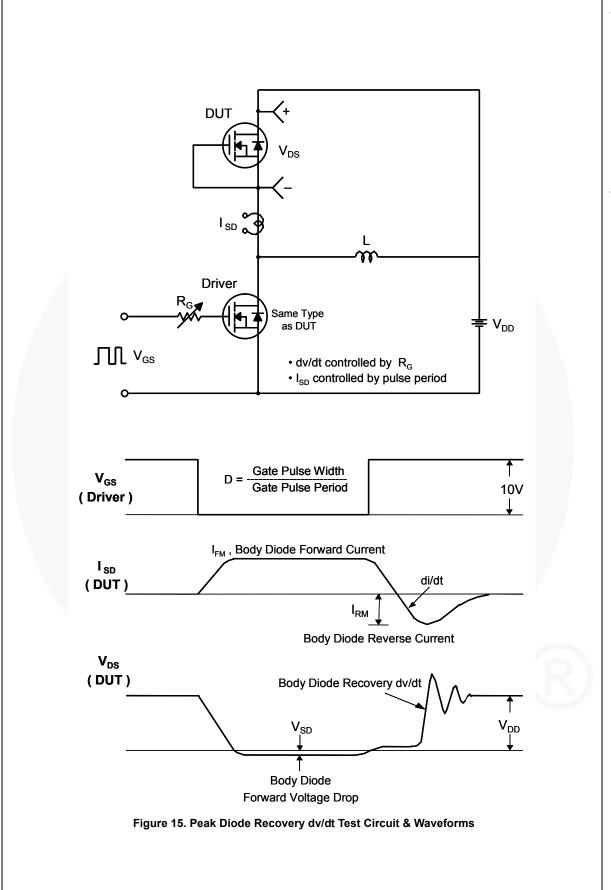


Figure 14. Unclamped Inductive Switching Test Circuit & Waveforms



Mechanical Dimensions

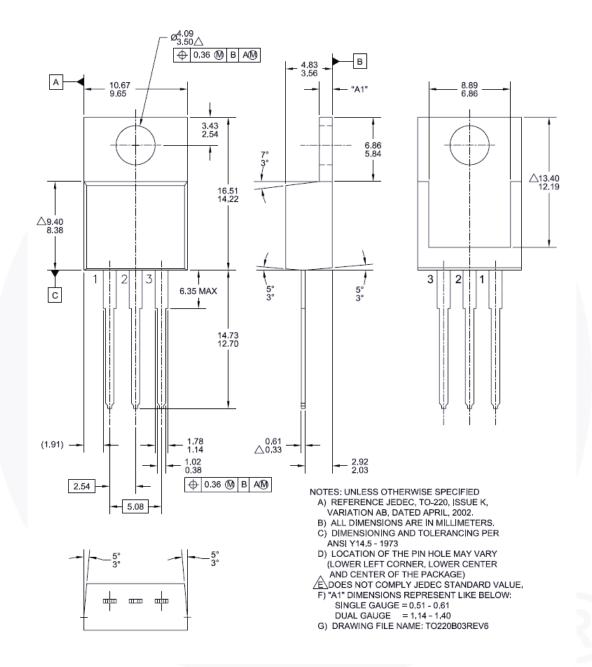


Figure 16. TO-220, Molded, 3-Lead, Jedec Variation AB

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